

CLAIMS

1. A display device, comprising:

a pixel part comprised of pixels arranged in a matrix and having a signal line arranged for each pixel string,

a clock generating means for generating a first clock signal serving as a reference for horizontal scanning and generating a second clock signal having the same period and having a smaller duty ratio than the first clock signal,

a shift register for performing a shift operation in synchronization with said first clock signal and outputting a shift pulse in sequence from the shift stages,

a first switch group for sampling said second clock signal in response to a shift pulse output in sequence from said shift register, and

a second switch group for sampling an input video signal in sequence in response to said second clock signal sampled by the switches of the first switch group and supplying the same to the signal lines of the pixel part.

2. A display device as set forth in claim 1, wherein a display element of each pixel of said pixel part is a liquid-crystal cell.

3. A method of driving a display device for obtaining shift pulses in sequence in synchronization with a first clock signal at the time of horizontal scanning with respect to a pixel part comprised by pixels arranged in a matrix and having a signal line arranged for each pixel string and for supplying a video signal to the signal line of said pixel part while sampling the video signal based on these shift pulses, comprising:

generating a second clock signal having the same period and having a smaller duty ratio than the first clock signal,

sampling the second clock pulse based on the shift pulse and using it as the sampling pulse, and

supplying the video signal to a signal line of the pixel part while sampling the video signal by this sampling pulse.

4. A method of driving a display device as set forth in claim 3, wherein a display element of each pixel of said pixel part is a liquid-crystal cell.

5. A projection type display device, comprising:

a clock generating means for generating a first clock signal serving as a reference for horizontal scanning and generating a second clock signal having the same period and having a smaller duty ratio than the first clock signal,

5 a display panel having a pixel part comprised  
by pixels arranged in a matrix and having a signal line  
arranged for each pixel string and a horizontal driving  
system for sampling said second clock signal based on a  
shift pulse obtained in sequence in synchronization with  
said first clock signal and sampling an input video  
signal in sequence in response to said sampled second  
clock signal and supplying the same to the signal lines  
of the pixel part,

10 an emitting means for emitting light to said  
display panel, and

a projecting means for projecting light passing  
through the display panel to a screen.

6. A projection type display device as set forth  
15 in claim 5, wherein a display element of each pixel of  
said pixel part is a liquid-crystal cell.